

What is Claimed is:

1. A circuit for varying the integration time of moving charges from a photodetector comprising:  
a first charge well for receiving moving charges from a photodetector;  
at least one additional charge well; and  
means for selectively switching said at least one additional charge well in parallel with said first charge well to vary the integration time of said moving charges.
2. The circuit of claim 1, wherein said first charge well comprises a capacitor.
3. The circuit of claim 1, wherein said at least one additional charge well comprises a capacitor.
4. A method of varying the integration time of moving charges from a photodetector comprising the steps of:  
supplying moving charges from a photodetector to an integration capacitance; and  
selectively varying said integration capacitance to vary the integration time of said moving charges.

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